## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of
Invention

SYSTEM AND METHOD FOR PREVENTING ELECTRIC ARCS IN CONNECTORS FEEDING POWER LOADS AND CONNECTOR USED

Application Number:

Date:

First Named Applicant:

Carles Borrego Bel

Confirmation Number:

Attorney Docket Number: 8142ES

I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Submitted By:	Elec. Sign.	Sign. Capacity
Bruce E Harang Registered Number: 29720	Bruce E Harang	Attorney

Documents being submitted:	Files		
us-request	8142ES-usrequ.xml		
	us-request.dtd		
	us-request.xsl		
us-fee-sheet	8142ES-usfees.xml		
	us-fee-sheet.xsl		
	us-fee-sheet.dtd		
us-ids	8142ES-usidst.xml		
	us-ids.dtd		
	us-ids.xsl		
us-declaration	Lear8142ES_Declaration_1.tif		
us-declaration	Lear8142ES_Declaration_2.tif		
us-declaration	Lear8142ES_Declaration_3.tif		
us-declaration	Lear8142ES_Declaration_4.tif		
application-body	Lear8142ES.s4w-trans.xml		
	us-application-body.xsl		
	application-body.dtd		
	wipo.ent		
	mathml2.dtd		
	mathml2-qname-1.mod		
	isoamsa.ent		
	isoamsb.ent		
	isoamsc.ent		
	isoamsn.ent		
	isoamso.ent		
	isoamsr.ent		
	isogrk3.ent		
	isomfrk.ent		
	isomopf.ent		
	isomscr.ent		
	isotech.ent		
	isobox.ent		
	isocyr1.ent		
	isocyr2.ent		
	isodia.ent		
	isolat1.ent		
	isolat2.ent		
	isonum.ent		
	isopub.ent		
	mmlextra.ent		
	mmlalias.ent		
	soextblx.dtd		
	Lear8142ES_Figs_1-3.tif		
	Lear8142ES_Figs_4.tif		
	Lear8142ES_Figs_5.tif		
	Lear8142ES_Figs_6.tif		
	Lear8142ES_Figs_7.tif		
Comments			